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Simulation of Daisy Chain Flip-Chip interconnections

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Introduction

Flip-Chip technologies are selected among Au bump thermocompression with and without NCA underfiller, anisotropic conductive adhesive bonding, and AuSn20 eutectic solder. Within Comsol framework the daisy chain simulations and a method for comparing single-bump resistance are implemented. Challenges:

• estimate more accurately the single-bump resistance

model current crowding effect

• model high aspect-ratio 3D model

Solution:

- simplified FEM model solved using AC/DC Comsol Multiphysics feature
- normalize experimental data with numerical correction factors calculated through FEM results post-processing

